can be enlarged to match the requirement of USPTO. However no content of the drawing is amended, only the original view in Fig. 4 is enlarged so as to be divided into Figs. 4, 4-1 and 4-2. Thus, it is assured that no new matter is added.

IN THE SPECIFICATION

(A) Please amend the description of the drawing as the following:

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a schematic view of a prior art game controller.

Fig. 2 is a schematic view showing the prior art racing car-used controller.

Fig. 3 is an electric block diagram of the resolution adjustable game controller of the present invention.

Figs. 4, 4-1, 4-2 are ~~Fig. 4 is~~ a circuit diagram of the resolution adjustable game controller of the present invention.

(B) Please amend the sixth paragraph in the "DETAIL

DESCRIPTION OF THE INVENTION" as the following:

With reference to Figs. 3, 4, 4-1, 4-2 ~~and 4~~, a circuit about the resolution adjustable game controller of the present invention is illustrated. In the circuit, the control circuit 14 is a microprocessor chip JCH089XX which is a commonly used program controller. The controller can receive

the signals from the plurality of original buttons 13. According to this embodiment, the control circuit 14 can receive trigger signals from the resolution button 12 so as to change the resolution of the linear controller 11 to another value. The resolution is outputted to the main frame 3 through the communication interface 15. The communication interface 15 is a PS CON for communicating the game controller 1 with the main frame 3. In this embodiment, two linear controllers 11 are equipped and a plurality of original buttons 13. One of the linear controllers 11 is controlled by the resolution button 12. In another embodiment, the resolution button 12 is added to the control circuit 14, which has the same function as above mentioned.